11/16/2018

The RBL reads first reads the first image header of MLO .from image header it will get important an information.

1. Load the address.
2. Total size of MLO.

MPL

Image header

SPL

Load \_addr=0x40000000

So RBL load the MLO to the internal Ram address indicated by image headerof the MLO.

**Summary : RBL**

* Stack Setup
* Watchdog timer 1 configuration (set to 3 minutes timeout).
* System clock configuration using PLL.
* Search memory devices or other bootable interfaces for MLO or SPL.
* Copy “MLO or SPL” in to the internal SRAM of the chip.
* Execute MLO or SPL.

**MLO/SPL JOB**

MLO or SPL initialize the SOC to point that U-boot can be loaded into external RAM (DDR memory).for a project you want to know proper clock frequency according to that reconfigure the PLL.

1.it does UART console initialization to print out the debug messages.

2.Reconfigures the PLL to desired value.

3.